

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	1185	257/\$.ccls. and (board carrier substrate) and memory adj module	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2003/10/1 8 18:18	
2	BRS	L2	729	361/\$.ccls. and (board carrier substrate) and memory adj module	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2003/10/1 8 18:19	
3	BRS	L3	441	438/\$.ccls. and (board carrier substrate) and memory adj module	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2003/10/1 8 18:20	
4	BRS	L4	229	174/\$.ccls. and (board carrier substrate) and memory adj module	USPA T; US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2003/10/1 8 18:20	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L5	105	29/\$.ccls. and (board carrier substrate) and memory adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 18:24	
6	BRS	L6	14	206/\$.ccls. and (board carrier substrate) and memory adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 18:25	
7	BRS	L7	12	269/\$.ccls. and (board carrier substrate) and memory adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 18:26	
8	BRS	L8	439	439/\$.ccls. and (board carrier substrate) and memory adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/18 18:26	